Inverter DC discrete rate

On the basis of traditional discrete sliding mode control, a new adaptive approach rate is introduced, which can dynamically adjust the control gain according to the distance between the ...

Digital design engineers, hardware engineers and electronics students need a deep understanding of inverter circuits to design efficient and reliable systems. This article provides an ...

A quasi-continuous-time approach to the design of a digital PID control for DC/AC inverters with LC filter that use semiconductor switches producing PWM output voltage is presented.

In Section 2, experimental inverter models are described, including the inverter bridge parameters and simplified Bode plots of the measurement channels. In Section 3, a discrete model of ...

Model predictive control (MPC) provides an at tractive solution for the single-stage dual-dc-port inverter (SDI) due to its capabilities to deal with multiobjective control under unbalanced dc-port voltages. ...

Infineon"s two-level, slew-rate control (2L-SRC) gate driver IC enables a reduction of the number of paralleled IGBTs through a slew-rate-control technique. The current capability of a ...

The MMC prototype has been used for testing both converter and system-level controls in a reduced-scale laboratory set up of a Multi-Terminal DC transmission network (MTDC).

Due to the complexity and variability of its operating environment, the internal and external parameters of the quasi-PIR controlled inverter are easy to change, which will cause that the Hopf ...

In this investigation, the most popular multilevel inverter topologies and control approaches have been analyzed.

Two simulation models, a discrete-time PWM and a continuous-time, of an inverter are presented, which are used to tune the PID controller and to evaluate the control performance.

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